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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Chan et al.
Serial No.: 10/709,784
For: LOGIC CIRCUITS HAVING LINEAR AND CELLULAR
GATE TRANSISTORS
Filed: May 27, 2004
Examiner: Don P. Le
Art Unit: 2819
Confirmation No.: 3783
Customer No.: 27,623
Attorney Docket No.: FIS920040113US1

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

STATEMENT ON THE REASONS FOR ALLOWANCE

Dear Sir:

Applicants note receipt of the Reasons for Allowance in the Notice of Allowance. Applicants respectfully submit that the claims of the instant application are allowable for at least the reasons apparent from review of the specification and prior art, as well as the reasons presented by the Examiner.

August 12, 2005

Respectfully submitted,

Charles N. J. Ruggiero
Reg. No. 28,468
Attorney for Applicant(s)
Ohlandt, Greeley, Ruggiero & Perle, L.L.P.
One Landmark Square, 10th floor
Stamford, CT 06901-2682
Tel: (203) 327-4500



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COMMISSIONER FOR PATENTS
P.O. Box 1450
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Attention: Official Draftsperson

CORRECTION OF INFORMALITIES

Sir:

In response to the informalities enclosed with the Notice of Allowability dated May 19, 2005, enclosed herewith are the formal drawings (2 sheets) for Figures 1 and 2 for the above-identified patent application.

Please charge any additional fees or credit any such fees, if necessary to Deposit Account No. 09-0458 in the name of International Business Machines Corporation (IBM). A duplicate copy of this sheet is attached.

Respectfully submitted,

Charles N. J. Ruggiero, Esq.
Reg. No. 28,468
Ohlandt, Greeley, Ruggiero & Perle, L.L.P.
One Landmark Square, 10th Floor
Stamford, Connecticut 06901-2682
(203) 327-4500

Date: August 12, 2005

CERTIFICATE OF HANDLING

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING HAND DELIVERED BY
ADDRESSED TO: MAIL STOP ISSUE FEE, COMMISSIONER FOR PATENTS,
P.O. BOX 1450, ALEXANDRIA, VA 22313-1450, ON August, 2005.

NAME

SIGNATURE

August 2005
DATE